

An Efficient LLL Gram Using Buffered Transformations

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Overview

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Lattice

Definition

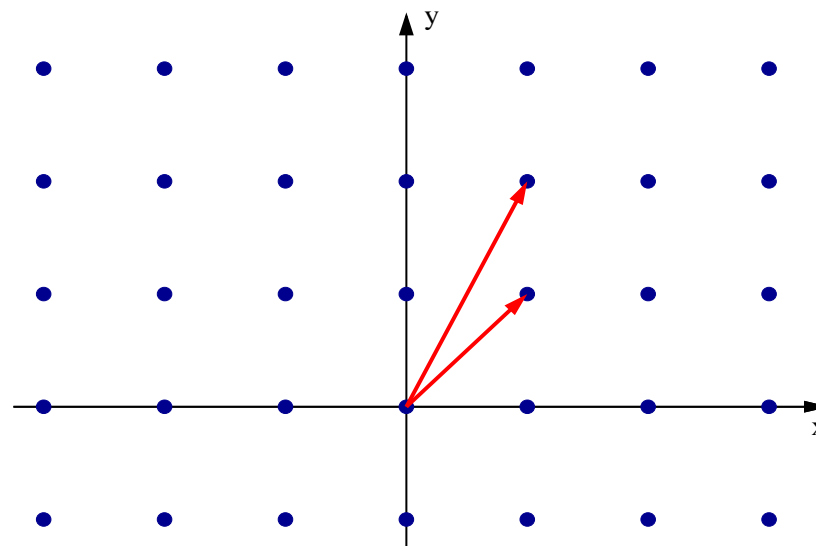
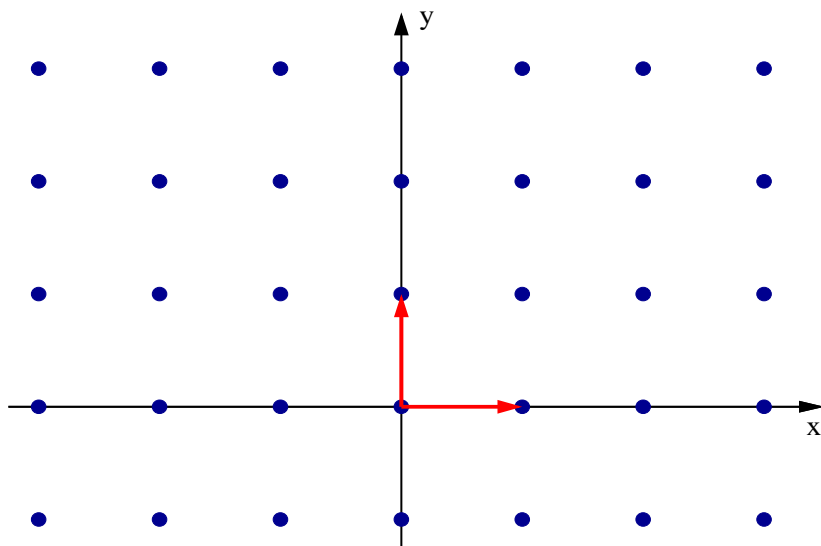
Let $n, k \in \mathbb{Z}$ with $k \leq n$. A **lattice** $L \subset \mathbb{R}^n$ is a discrete, additive subgroup of \mathbb{R}^n , such that

$$L = \left\{ \sum_{i=1}^k x_i \underline{b}_i \mid x_i \in \mathbb{Z}, i = 1, \dots, k \right\},$$

where $\underline{b}_1, \underline{b}_2, \dots, \underline{b}_k \in \mathbb{R}^n$ are linearly independent vectors.

$B = (\underline{b}_1, \dots, \underline{b}_k) \in \mathbb{R}^{n \times k}$ is a **basis** of the k -dimensional lattice L .

Example



$$L_1 = \left\{ \begin{pmatrix} x \\ y \end{pmatrix} \mid \begin{pmatrix} x \\ y \end{pmatrix} = c \begin{pmatrix} 1 \\ 0 \end{pmatrix} + d \begin{pmatrix} 0 \\ 1 \end{pmatrix}; c, d \in \mathbb{Z} \right\}$$
$$L_2 = \left\{ \begin{pmatrix} x \\ y \end{pmatrix} \mid \begin{pmatrix} x \\ y \end{pmatrix} = c \begin{pmatrix} 1 \\ 1 \end{pmatrix} + d \begin{pmatrix} 1 \\ 2 \end{pmatrix}; c, d \in \mathbb{Z} \right\}$$

Preliminaries

Lattice basis reduction: Find a basis $B' = (\underline{b}'_1, \dots, \underline{b}'_k)$ for lattice $L(B)$ with $B'U = B$ (U unimodular) such that the basis vectors are as short and as orthogonal to each other as possible.

Gram-Schmidt orthogonalization:

$$\begin{aligned} \underline{b}_1^* &= \underline{b}_1 & \underline{b}_i^* &= \underline{b}_i - \sum_{j=1}^{i-1} \mu_{ij} \underline{b}_j^* & \text{for } 2 \leq i \leq k \\ \mu_{ij} &= \frac{\langle \underline{b}_i, \underline{b}_j^* \rangle}{\|\underline{b}_j^*\|^2} & & \text{for } 1 \leq j < i \leq k \end{aligned}$$

Definition (LLL-Reduction)

A basis $B = (\underline{b}_1, \dots, \underline{b}_k)$ of the lattice $L \subset \mathbb{R}^n$ is called *LLL-reduced* for reduction parameter $\frac{1}{4} < \delta < 1$ if the following holds:

$$|\mu_{ij}| \leq \frac{1}{2} \quad \text{for } 1 \leq j < i \leq k$$

$$\|\underline{b}_i^* + \mu_{ii-1} \underline{b}_{i-1}^*\|^2 \geq \delta \|\underline{b}_{i-1}^*\|^2 \quad \text{for } 1 < i \leq k$$

LLL Algorithm – Schnorr-Euchner

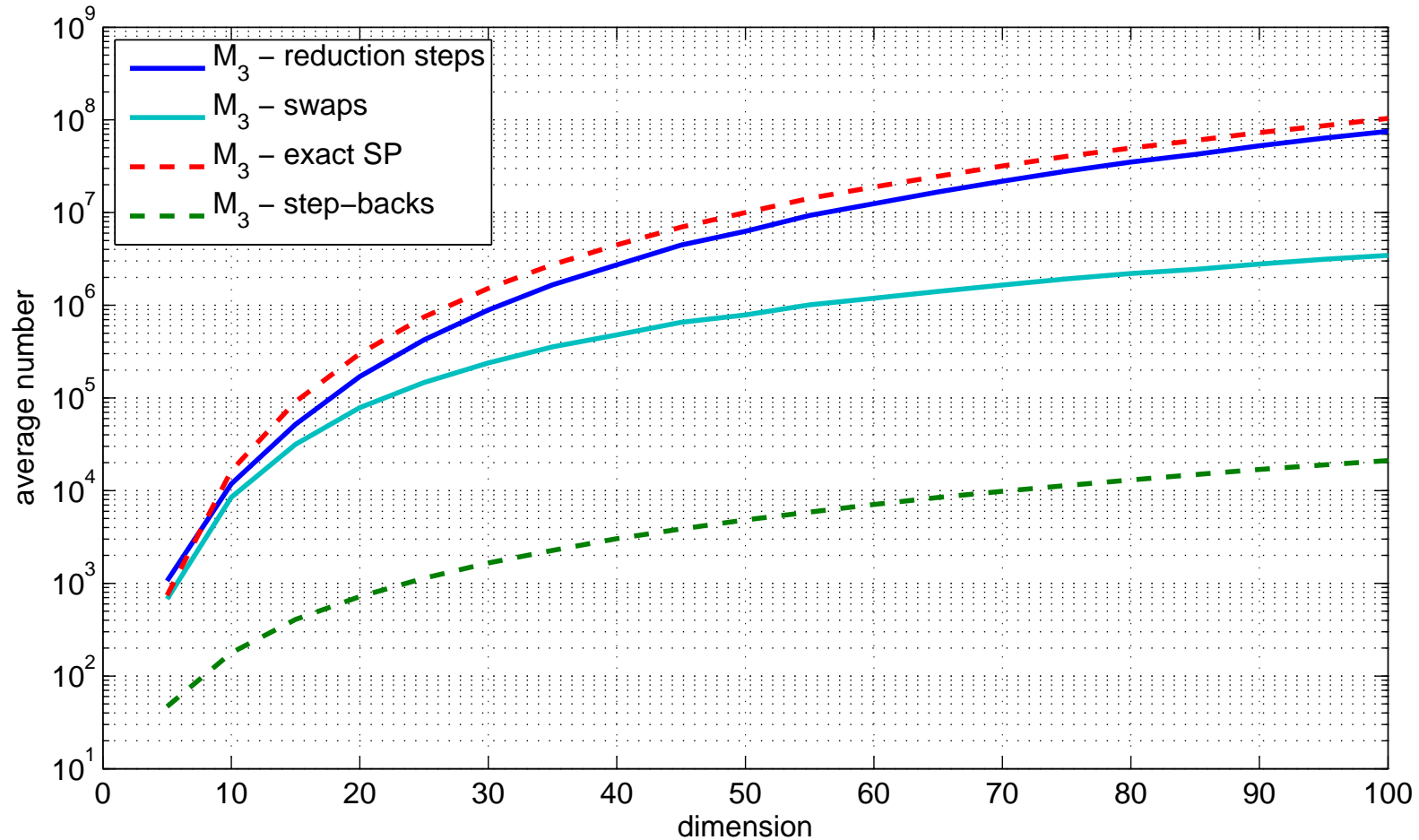
INPUT: Lattice basis $B = (\underline{b}_1, \dots, \underline{b}_k) \in \mathbb{Z}^{n \times k}$, δ

OUTPUT: LLL-reduced lattice basis

```
(1) APPROX_BASIS( $B'$ ,  $B$ )
(2)  $B_1 = \|\underline{b}'_1\|^2$ ,  $i = 2$ 
(3)  $F_c = false$ ,  $F_r = false$ 
(4) while ( $i \leq k$ ) do
(5)    $B_i = \|\underline{b}'_i\|^2$ 
(6)   for ( $2 \leq j \leq i$ ) do
(7)     if ( $|\langle \underline{b}'_i, \underline{b}'_j \rangle| < 2^{\frac{r}{2}} \|\underline{b}'_i\| \|\underline{b}'_j\|$ ) then
(8)        $s = \text{APPROX\_VALUE}(\langle \underline{b}'_i, \underline{b}'_j \rangle)$ 
(9)     else
(10)       $s = \langle \underline{b}'_i, \underline{b}'_j \rangle$ 
(11)       $\mu_{ij} = (s - \sum_{m=1}^{j-1} \mu_{jm} \mu_{im} B_m) / B_j$ 
(12)       $B_i = B_i - \mu_{ij}^2 B_j$ 
(13)       $\mu_{ii} = 1$ 
(14)    for ( $i > j \geq 1$ ) do
(15)      if ( $|\mu_{ij}| > \frac{1}{2}$ ) then
(16)         $F_r = true$ 
(17)      if ( $|\lceil \mu_{ij} \rceil| > 2^{\frac{r}{2}}$ ) then
(18)         $F_c = true$ 
(19)         $\underline{b}_i = \underline{b}_i - \lceil \mu_{ij} \rceil \underline{b}_j$ 
(20)        for ( $1 \leq m \leq j$ ) do
(21)           $\mu_{im} = \mu_{im} - \lceil \mu_{ij} \rceil \mu_{jm}$ 
(22)        if ( $F_r = true$ ) then
(23)          APPROX_VECTOR( $\underline{b}'_i, \underline{b}_i$ )
(24)           $F_r = false$ 
(25)        if ( $F_c = true$ ) then
(26)           $i = \max\{i - 1, 2\}$ 
(27)           $F_c = false$ 
(28)        else
(29)          if ( $B_i < (\delta - \mu_{ii-1}^2) B_{i-1}$ ) then
(30)            SWAP( $\underline{b}_{i-1}, \underline{b}_i$ )
(31)            if ( $i = 2$ ) then
(32)               $B_1 = \|\underline{b}'_1\|^2$ 
(33)               $i = \max\{i - 1, 2\}$ 
(34)            else
(35)               $i = i + 1$ 
```

exact scalar products, step-backs, reduction steps, swaps

LLL Algorithm – Correction Steps



- Correction steps (**exact scalar products** and **step-backs**) affect running time
- Exact scalar products have bigger impact.

LLL Algorithm – Gram Matrix

Definition

For a lattice L with basis $B = (\underline{b}_1, \dots, \underline{b}_k) \in \mathbb{R}^{n \times k}$, the corresponding Gram matrix G is defined as $G = B^T B$.

- Gram matrix is the matrix of scalar products, therefore symmetric

Advantage:

- Avoid expensive exact scalar products.

Problem:

- Gram matrix not of interest: update of Gram matrix and lattice basis necessary.

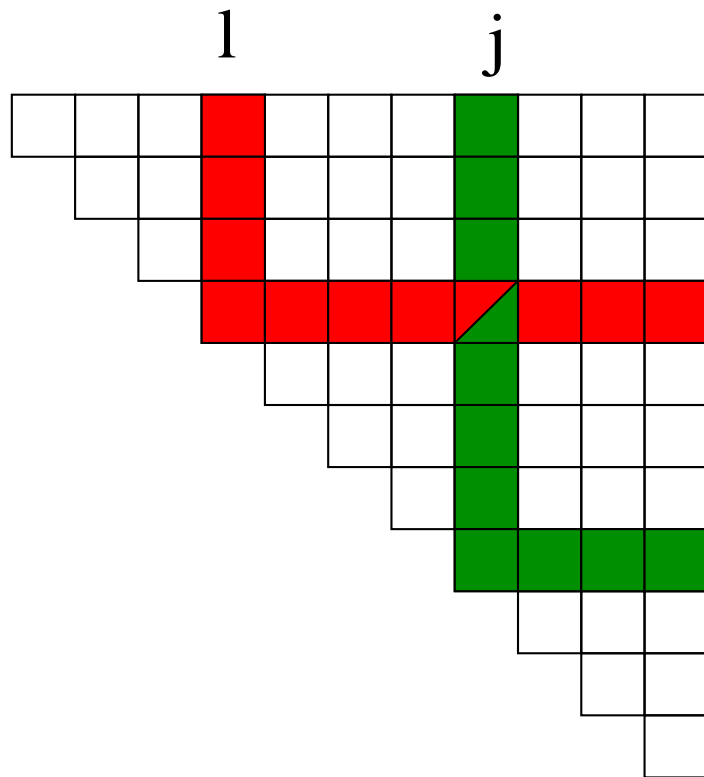
LLL Algorithm – Gram Variant

INPUT: Lattice basis $B = (\underline{b}_1, \dots, \underline{b}_k) \in \mathbb{Z}^{n \times k}, \delta$

OUTPUT: LLL-reduced lattice basis

```
(1) COMPUTE_GRAM(A, B)
(2) APPROX_BASIS_GRAM(A', A)
(3)  $R_{11} = A'_{11}, i = 2$ 
(4)  $F_c = false, F_r = false$ 
(5) while ( $i \leq k$ ) do
(6)    $S_1 = R_{ii}$ 
(7)   for ( $2 \leq j \leq i$ ) do
(8)      $R_{ij} = A'_{ji} - \sum_{m=1}^{j-1} R_{im} \mu_{im}$ 
(9)      $\mu_{ij} = \frac{R_{ij}}{R_{jj}}$ 
(10)     $R_{ii} = R_{ii} - R_{ij} \mu_{ij}$ 
(11)     $S_{j+1} = R_{ii}$ 
(12)     $\mu_{ii} = 1$ 
(13)    for ( $i > j \geq 1$ ) do
(14)      if ( $|\mu_{i,j}| > \frac{1}{2}$ ) then
(15)         $F_r = true$ 
(16)        if ( $|\mu_{ij}| > 2^{\frac{r}{2}}$ ) then
(17)           $F_c = true$ 
(18)           $b_i = b_i - \lceil \mu_{ij} \rceil b_j$ 
(19)          REDUCE_GRAM(A, i,  $\lceil \mu_{ij} \rceil, j$ )
(20)        for ( $1 \leq m \leq j$ ) do
(21)           $\mu_{im} = \mu_{im} - \lceil \mu_{ij} \rceil \mu_{im}$ 
(22)        if ( $F_r = true$ ) then
(23)          APPROX_VECTOR_GRAM(A', A, i)
(24)           $F_r = false$ 
(25)        if ( $F_c = true$ ) then
(26)           $i = \max(i - 1, 2)$ 
(27)           $F_c = false$ 
(28)        else
(29)           $i' = i$ 
(30)          while ( $(i > 1) \wedge (\delta \cdot R_{(i-1)(i-1)} > S_{i-1})$ ) do
(31)            SWAP( $b_i, b_{i-1}$ )
(32)            SWAP_GRAM(A,  $i - 1, i$ )
(33)             $i = i - 1$ 
(34)          if ( $i \neq i'$ ) then
(35)            if ( $i = 1$ ) then
(36)               $R_{11} = A'_{11}, i = 2$ 
(37)            else
(38)               $i = i + 1$ 
```

LLL Algorithm – Gram Variant



Algorithm: REDUCE_GRAM($A, l, \lceil \mu_{ij} \rceil, j$)

INPUT: Gram matrix A , indices $l, j, \lceil \mu_{ij} \rceil$

OUTPUT: Gram matrix A

- (1) $T = A_{l,l} - 2 \cdot \lceil \mu_{ij} \rceil \cdot A_{j,l} - \lceil \mu_{ij} \rceil^2 \cdot A_{j,j}$
- (2) **for** ($1 \leq m < j$) **do**
- (3) $A_{m,l} = A_{m,l} - \lceil \mu_{ij} \rceil \cdot A_{m,j}$
- (4) **for** ($j \leq m < l$) **do**
- (5) $A_{m,l} = A_{m,l} - \lceil \mu_{ij} \rceil \cdot A_{j,m}$
- (6) **for** ($l + 1 \leq m < k$) **do**
- (7) $A_{l,m} = A_{l,m} - \lceil \mu_{ij} \rceil \cdot A_{j,m}$
- (8) $A_{l,l} = T$

LLL Algorithm – Optimizations (1)

1. Buffer transformation to lattice basis using machine integers

Algorithm: BUFFERED_TRANSFORM($B, i, \lceil \mu_{ij} \rceil, j$)

INPUT: Lattice Basis $B = (\underline{b}_1, \dots, \underline{b}_k) \in \mathbb{Z}^{n \times k}$, indices $i, j, \lceil \mu_{ij} \rceil$

OUTPUT: Lattice Basis B

- | | |
|---|---|
| (1) if ($(Tmax_i + \lceil \mu_{ij} \rceil Tmax_j) \geq 2^{m-1}$) then | (13) if ($\lceil \mu_{ij} \rceil > 2^{m-1} - 1$) then |
| (2) for ($pos_{min} \leq x \leq pos_{max}$) do | (14) $\underline{b}_i = \underline{b}_i - \lceil \mu_{ij} \rceil \cdot \underline{b}_j$ |
| (3) for ($1 \leq z \leq n$) do | (15) return |
| (4) $B'_{xz} = 0$ | (16) $\underline{t}_i = \underline{t}_i - \lceil \mu_{ij} \rceil \cdot \underline{t}_j$ |
| (5) for ($pos_{min} \leq y \leq pos_{max}$) do | (17) $Tmax_i = Tmax_i + \lceil \mu_{ij} \rceil \cdot Tmax_j$ |
| (6) for ($1 \leq z \leq n$) do | (18) if ($pos_{max} < i$) then |
| (7) $B'_{xz} = B'_{xz} + T_{xy} \cdot B_{yz}$ | (19) $pos_{max} = i$ |
| (8) $B \leftrightarrow B'$ | (20) if ($pos_{min} > j$) then |
| (9) $T = I_n$ | (21) $pos_{min} = j$ |
| (10) $Tmax = (1, \dots, 1)^T$ | |
| (11) $pos_{max} = i$ | |
| (12) $pos_{min} = j$ | |

LLL Algorithm – Optimizations (2)

2. Avoid expensive operations (Multiplications)

3. Vectorization (Multimedia Streaming Extensions)

```
...
(7) if ( $T_{xy} \neq 0$ ) then
(8)   if ( $T_{xy} = 1$ ) then
(9)     for ( $1 \leq z \leq n$ ) do
(10)       $B'_{xz} = B'_{xz} + B_{yz}$ 
(11)   else
(12)     if ( $T_{xy} = -1$ ) then
(13)       for ( $1 \leq z \leq n$ ) do
(14)         $B'_{xz} = B'_{xz} - B_{yz}$ 
(15)     else
(16)       for ( $1 \leq z \leq n$ ) do
(17)         $B'_{xz} = B'_{xz} + T_{xy} \cdot B_{yz}$ 
...
```

```
...
(16) for ( $1 \leq l \leq n; l+ = 4$ ) do
(17)    $T_{i,l} = T_{i,l} - \lceil \mu_{ij} \rceil \cdot T_{j,l}$ 
(18)    $T_{i,l+1} = T_{i,l+1} - \lceil \mu_{ij} \rceil \cdot T_{j,l+1}$ 
(19)    $T_{i,l+2} = T_{i,l+2} - \lceil \mu_{ij} \rceil \cdot T_{j,l+2}$ 
(20)    $T_{i,l+3} = T_{i,l+3} - \lceil \mu_{ij} \rceil \cdot T_{j,l+3}$ 
...
```

LLL Algorithm – Related Work

NTL (by Victor Shoup)

- based on Schnorr-Euchner, with lots of improvements.
- modified heuristic to reduce number of exact scalar products

fpLLL (by Damien Stehlé and Phong Nguyen)

- uses Gram matrix
- new (δ, η) reduction condition with $\delta \in (\frac{1}{2}, 1)$ and $\eta > \frac{1}{2}$
- provable, but weaker than original LLL ($\eta = 0.5$)

Segment LLL-Reduction (by Schnorr Group)

- weaker reduction condition
- Householder orthogonalization

LLL Algorithm – Results (1)

Lower triangular matrices U_j , upper triangular matrices V_j with $1 \leq j \leq 2$, permutation matrices P_i for $1 \leq i \leq 4$, create three types of unimodular lattice bases:

- $M_1: B = (U_1 \cdot V_1)$
- $M_2: B = (U_1 P_1 \cdot V_1 P_2)$
- $M_3: B = (U_1 P_1 \cdot V_1 P_2) \cdot (V_2 P_3 \cdot U_2 P_4)$

Test Parameter:

- 1000 lattice bases for each type and dimension 5, 10, 15, ..., 100
- maximal bit length of lattice basis entries is 500

LLL Algorithm – Results (2)

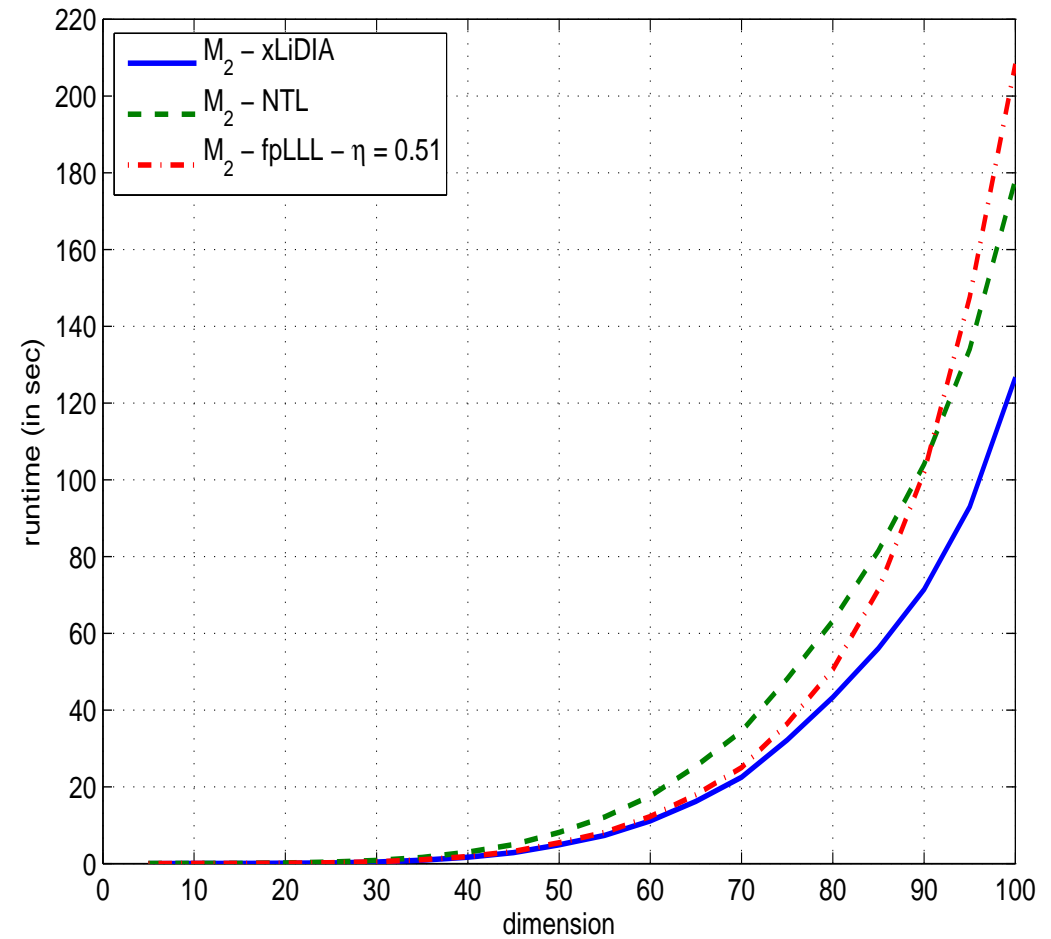
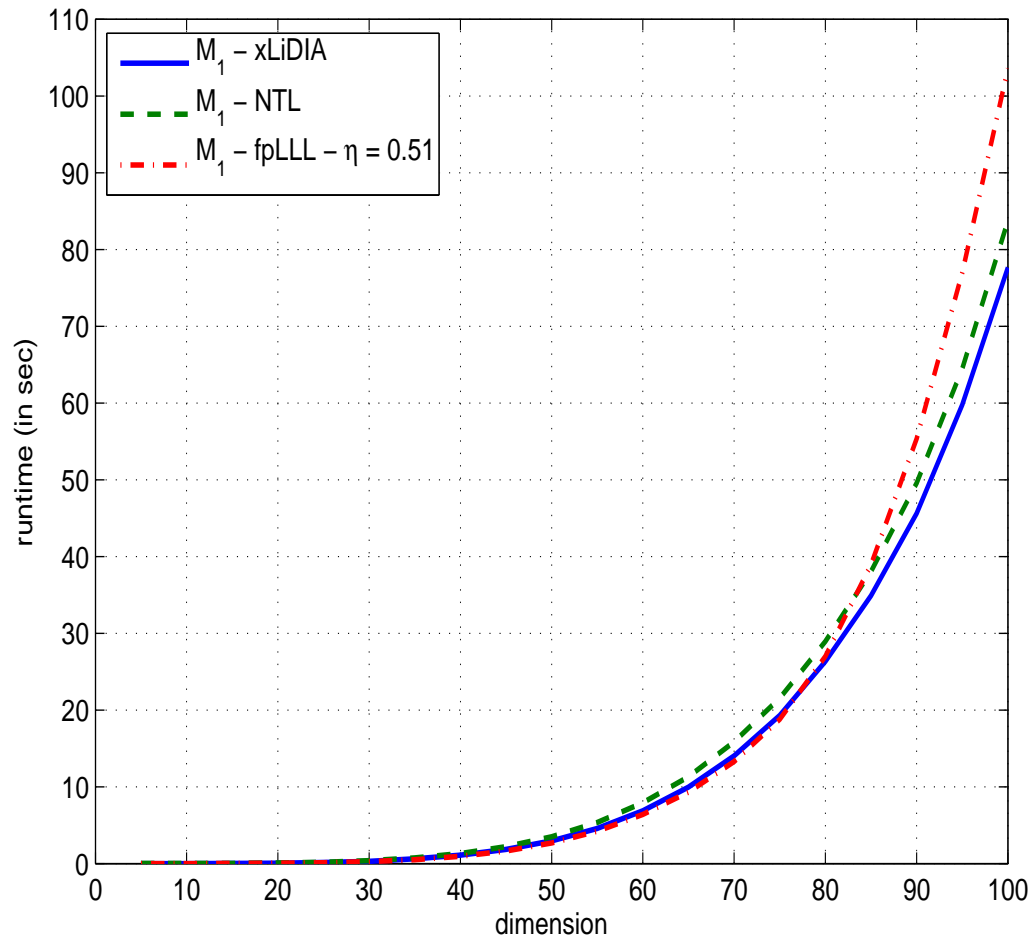
Test Setup:

- LLL_FP from NTL 5.4 (by Victor Shoup)
- proved variant for fpLLL 1.3 with $\eta = 0.51$ (by Damien Stehlé and Phong Nguyen)
- xLiDIA implementation (modified, stripped down version of LiDIA)
- GCC 4.1, GMP 4.2.1 with additional AMD64 patch (by Pierrick Gaudry)

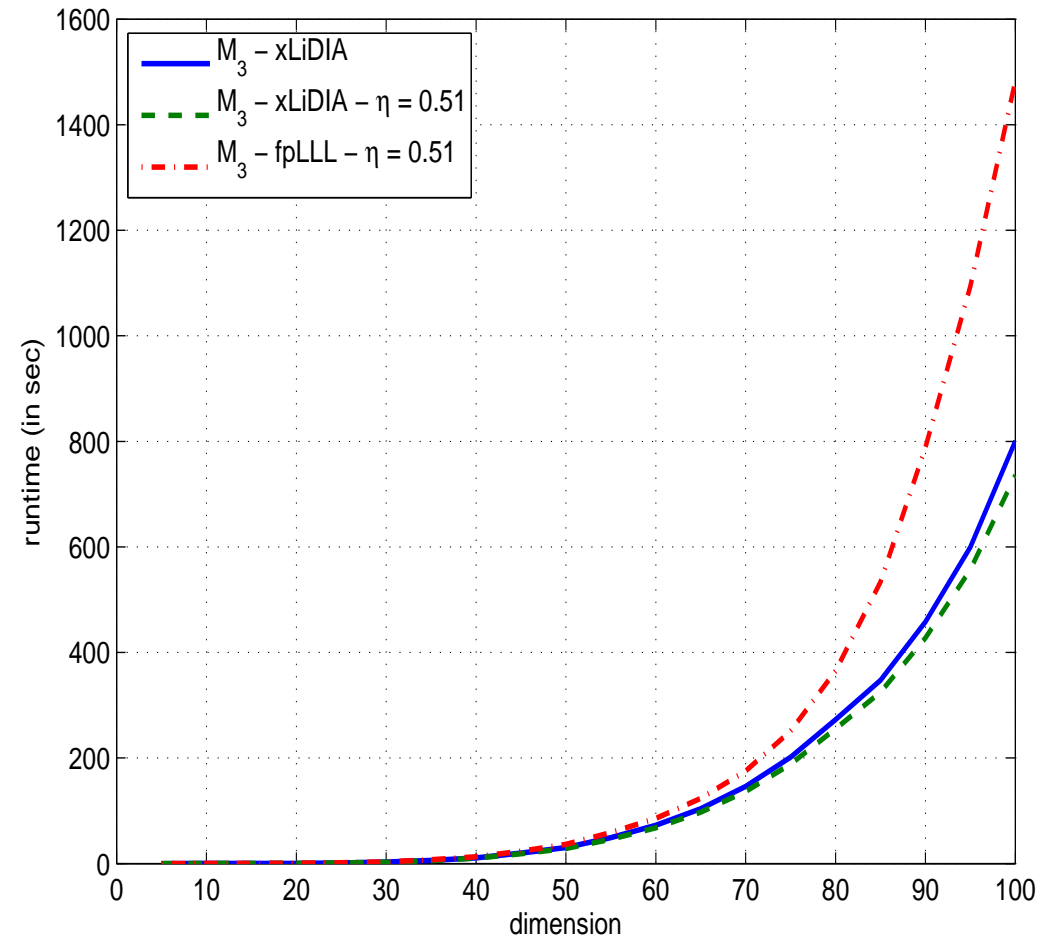
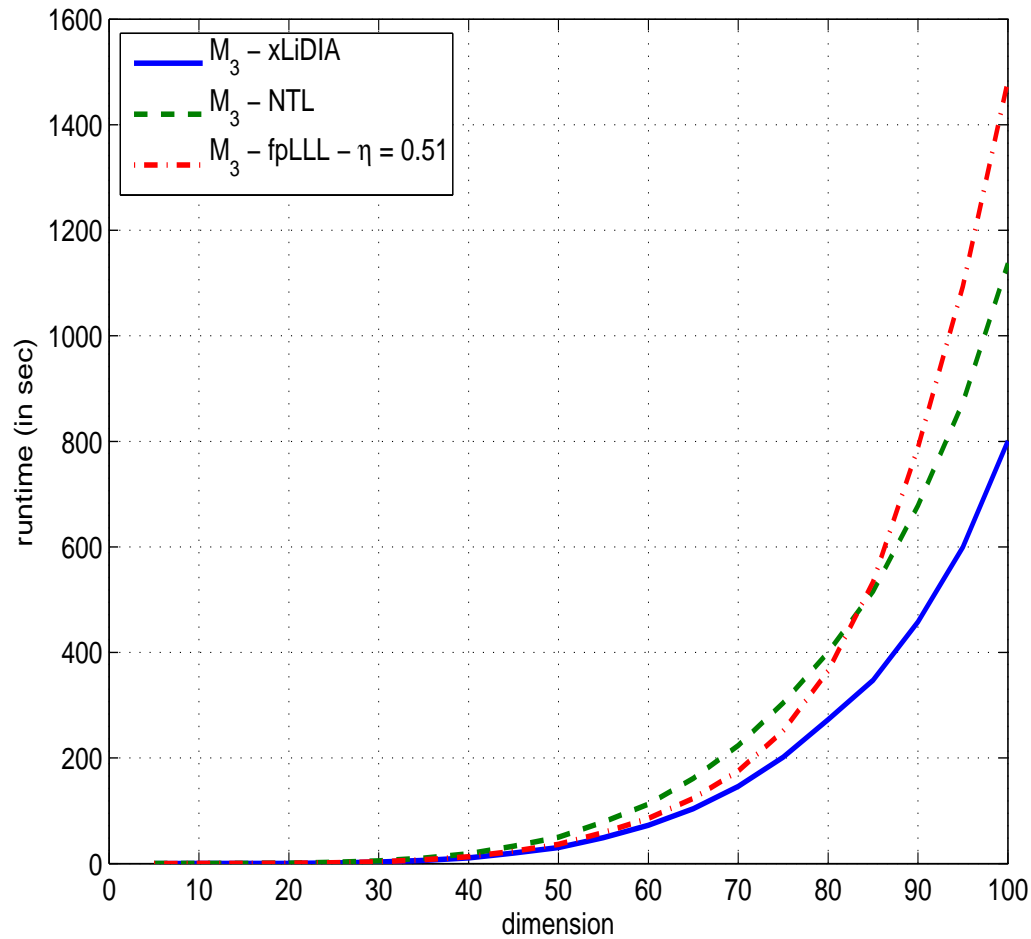
Test System:

- Test System: Sun X4100 Server, AMD Opteron 2.2 GHz and 4GB RAM, Solaris 10

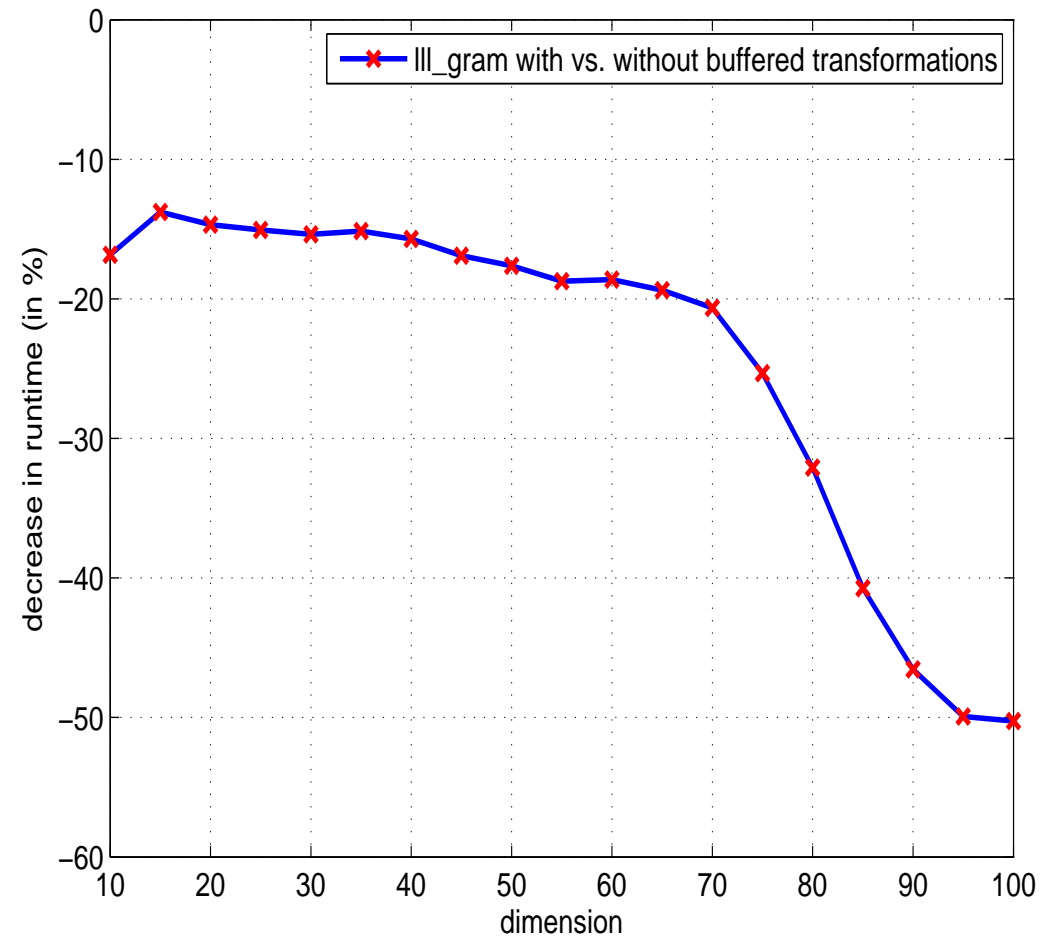
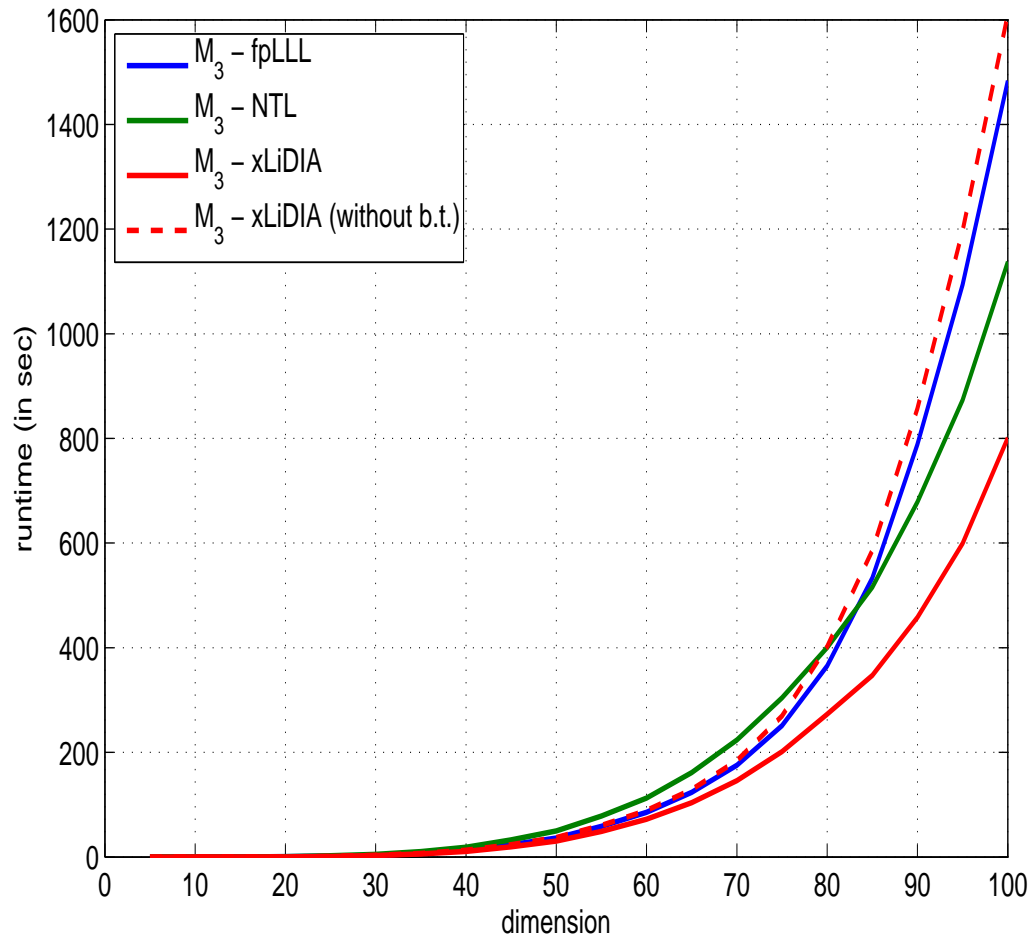
LLL Algorithm – Results (3)



LLL Algorithm – Results (4)

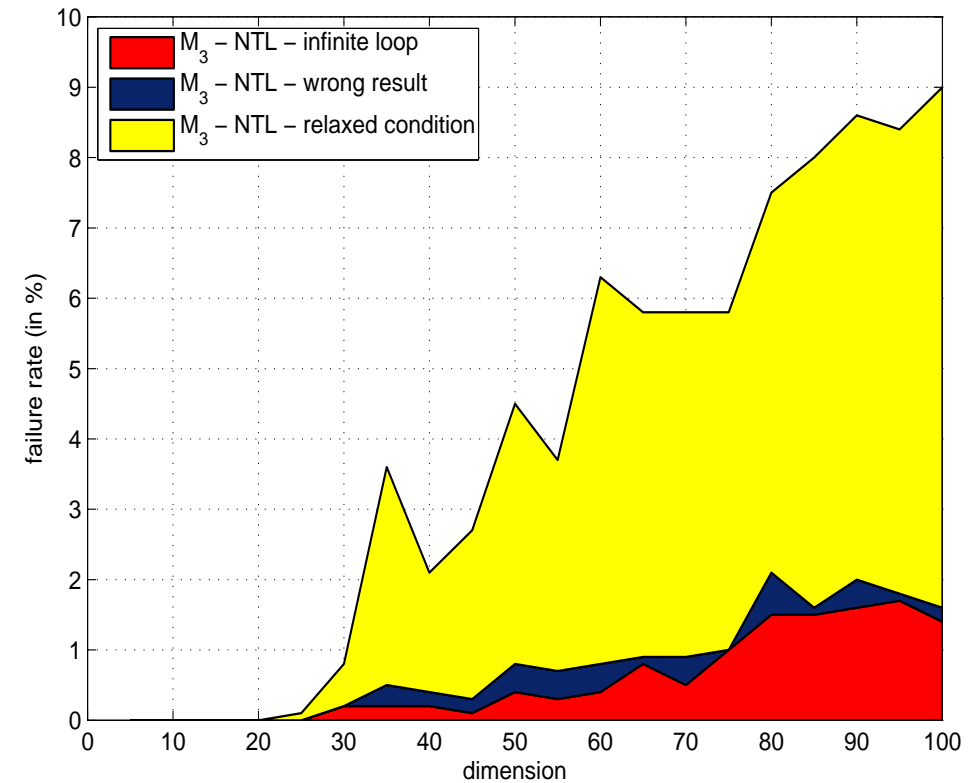
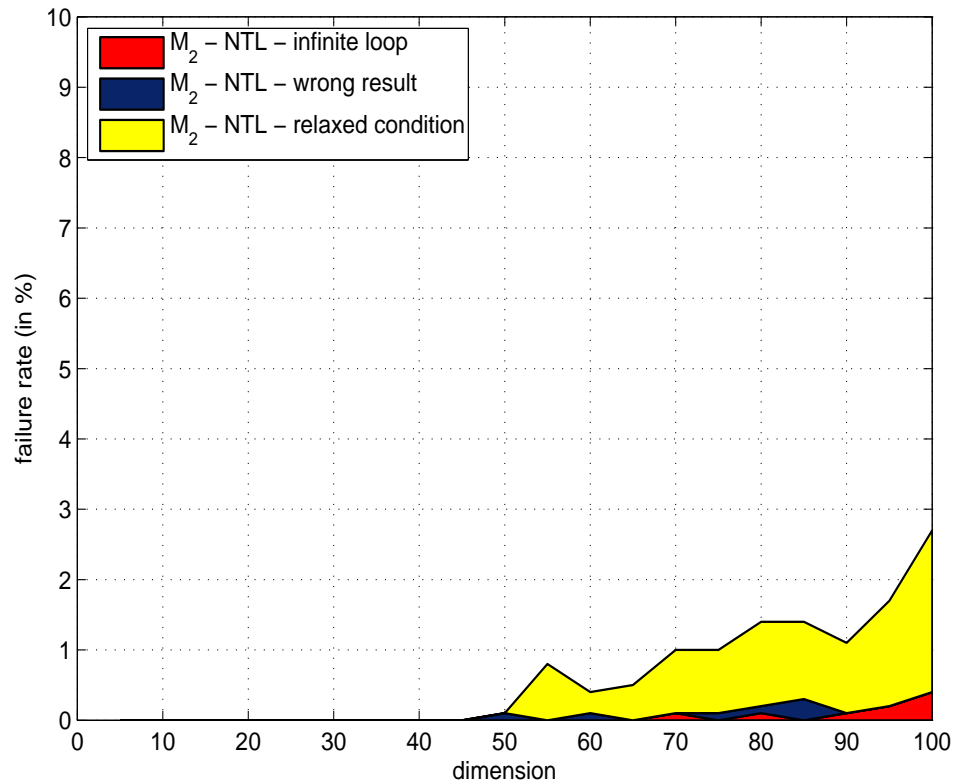


LLL Algorithm – Results (5)



● LLL Gram with and without buffered transformations.

LLL Algorithm – Results (6)



- fast and heuristic variant of fpLLL fails for small bases (dimension 10 with entries of maximum bit length of 100 bits)

Conclusion/Future Work

Conclusion

- Our implementation outperforms NTL and fpLLL
- Buffered transformation offer massive runtime improvement
- Keep original LLL condition (compared to fpLLL)
- Improved stability (compared to NTL)

Future Work

- Parallel version (multi-core CPUs)
- Extend use of machine-type doubles

References

- [1] W. Backes and S. Wetzel. Heuristics on Lattice Basis Reduction in Practice. *ACM Journal on Experimental Algorithms*, 7, 2002.
- [2] A. Lenstra, H. Lenstra, and L. Lovász. Factoring Polynomials with Rational Coefficients. *Math. Ann.*, 261:515–534, 1982.
- [3] P. Nguyen and D. Stehlé. Floating-Point LLL Revisited. In *Proceedings of Eurocrypt 2005*, volume 3494 of *LNCS*, pages 215–233. Springer, 2005.
- [4] C. Schnorr and M. Euchner. Lattice Basis Reduction: Improved Practical Algorithms and Solving Subset Sum Problems. In *Proceedings of Fundamentals of Computation Theory '91*, volume 529 of *LNCS*, pages 68–85. Springer, 1991.
- [5] D. Stehlé. The New LLL Routine in the Magma Computational Algebra System. *Magma 2006 Conference*.
<http://magma.maths.usyd.edu.au/Magma2006/>.
- [6] S. Wetzel. *Lattice Basis Reduction Algorithms and their Applications*. PhD thesis, Universität des Saarlandes, 1998.
- [7] fpLLL - Homepage (Damien Stehlé), July 2007. <http://www.loria.fr/~stehle/>.
- [8] AMD64 patch for GMP 4.2, July 2007. http://www.loria.fr/~gaudry/mpn_AMD64/index.html.
- [9] NTL - Homepage, July 2007. <http://www.shoup.net/ntl/>.